PATENT ABSTRACTS OF JAPAN

(11)Publication number :　　59-135563

(43)Date of publication of application : 03.08.1984

(51)Int.CI.	G06F 13/04 G06F 3/00 G11C 9/06

(21)Application number : **58-008741**　　(71)Applicant : **HITACHI LTD**

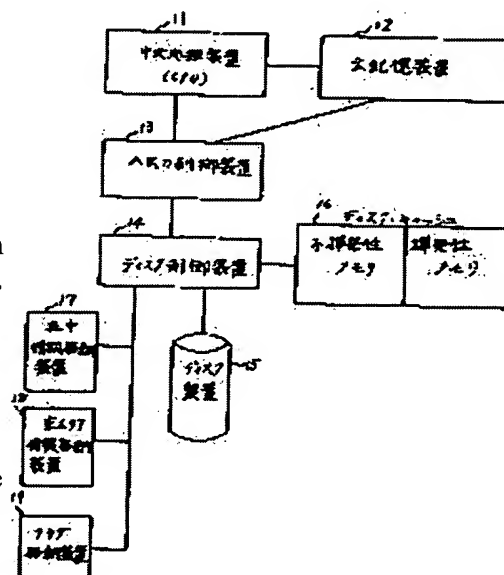(22)Date of filing :　　**24.01.1983**　　(72)Inventor : **YAMAMOTO AKIRA**
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(54) COMPUTER SYSTEM HAVING DISK CACHE DEVICE

(57)Abstract:
PURPOSE: To accelerate not only input processing speed but output processing speed by using a nonvolatile memory part and a volatile memory part to form a disk cache device.
CONSTITUTION: A disk controller 14 receives an output request from an input/ output controller 13 and then turns off the two bits of a flag device. Then an included information storing dvice 17 is used to check whether the data to be written is included in the volatile part of a disk cache device 16. If not, the volatile memory transfer bit of a flag device 19 is turned on. While an idle area is secured for the nonvolatile part of the device 16 if the data is included in the volatile part of the device 16. In this case, an idle area bit is turned on for the device 19. Then the data is transferred, and at the same time the writing is carried out to the volatile part without fail. While the writing is carried out to the nonvolatile part only when the transfer bit of the nonvolatile memory of the device 19 is turned on. When the data transfer is over, it is informed to the controller 13.

LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of

rejection]
[Date of requesting appeal against examiner's
decision of rejection]
[Date of extinction of right]